

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of

Inventors: Yuri Mirgorodski, et al.

Application No.: 10/664,469

Filed: September 17, 2003

For: METHOD OF PMOS STACKED-GATE MEMORY CELL PROGRAMMING

ENHANCEMENT UTILIZING STAIR-LIKE PULSES OF CONTROL GATE VOLTAGE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Group Art Unit: 2818

Examiner: Unknown

CHANGE OF ATTORNEY OR AGENT'S ADDRESS IN APPLICATION (37 CFR 1.8(a))

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on June 7, 2004.

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